

PRO
U.S.
JC654 09/752538
• 3/28/00

ISSUE CLASSIFICATION

U.S. UTILITY Patent Application

<i>Hklm</i>	O.I.P.E.	PATENT DATE
SCANNED <i>8/81</i>	Q.A. <i>TC</i>	

APPLICATION NO. 09/752538	CONT/PRIOR	CLASS 174	SUBCLASS 262	ART UNIT 2841 2847	EXAMINER CONEO	I	PATENT
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TS Stephen Oh

Layered circuit boards and methods of production thereof

PTO-2040
12/89

ISSUING CLASSIFICATION

Continued on Issue Slip Inside File Jacket

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____				ISSUE FEE	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
				(Legal Instruments Examiner)	(Date)

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